

AMENDMENTS TO THE SPECIFICATION

1. Please amend the paragraph beginning on line 13 of page 6 as follows:

Figure 5A shows a cross-sectional view of a printed circuit board including a capacitor having a plurality of via coupling the ground pad to the reference voltage plane. Figure 5B shows a cross-sectional view of the printed circuit board (as previously shown in Figure 5A) having a ground pad and trace coupling the capacitor to the reference voltage plane. ~~Figure 5C shows a cross-sectional view of the capacitor previously shown in Figures 5A and 5B including a ground plane.~~

2. Please amend the paragraph beginning on line 18 of page 10 as follows:

Figure 5B illustrates one embodiment of the present disclosure. Figure 5B shows a portion of printed circuit board 502. In the embodiment depicted in Figure 5B, trace 590 is shown embedded in the surface of printed circuit board 502. In the embodiment shown in Figure 5B, ground pad 520 is electrically coupled to trace 590 which has a reduced length to minimize inductance. Trace 590 is electrically coupled to via 580. As previously discussed (refer to Equation 1) reducing the inductance of trace 590 reduces overall impedance. ~~Figure 5C shows a portion of the printed circuit board previously shown in Figure 5B including ground plane 551. In this enablement, ground plane 551 replaces reference voltage plane 550.~~